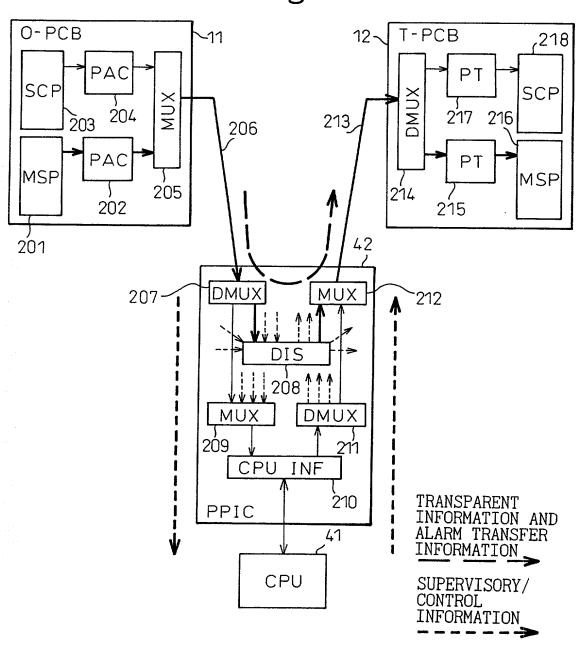


Fig.2



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Fig.3A

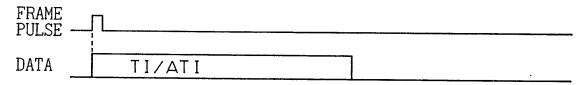


Fig.3B

ATM CP CH TI/ATI

Fig.3C

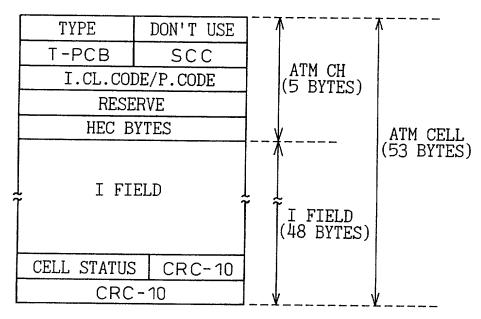
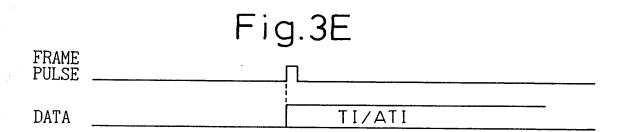
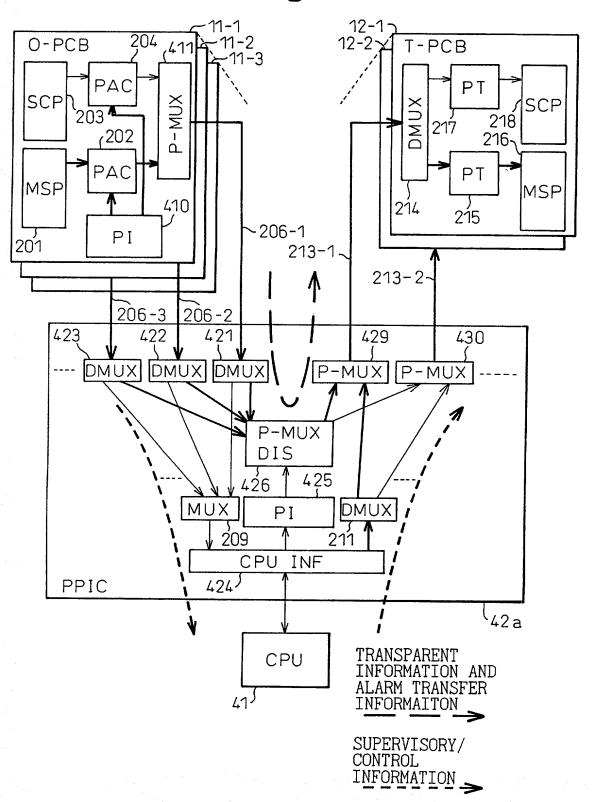


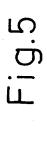
Fig.3D

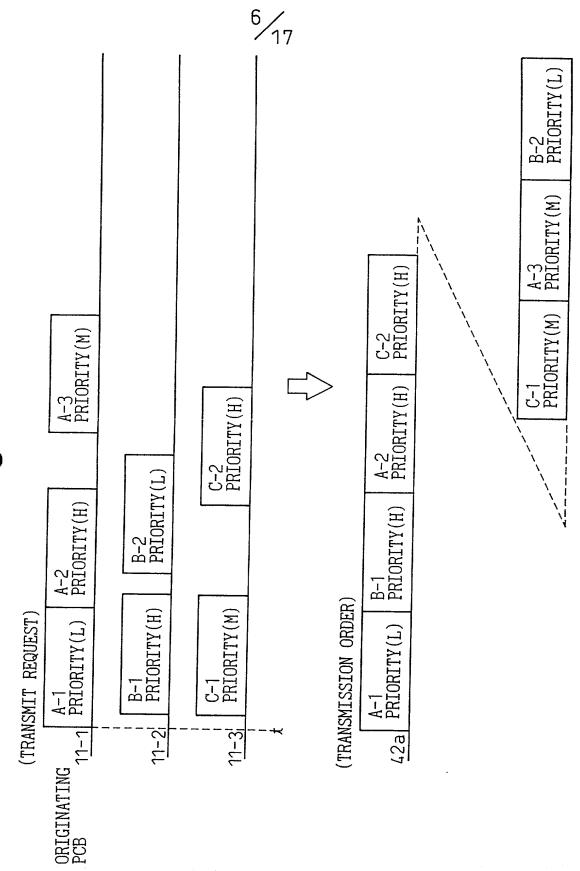
ATM CFII	
	TI/ATI
PACKET	117 A 1 1

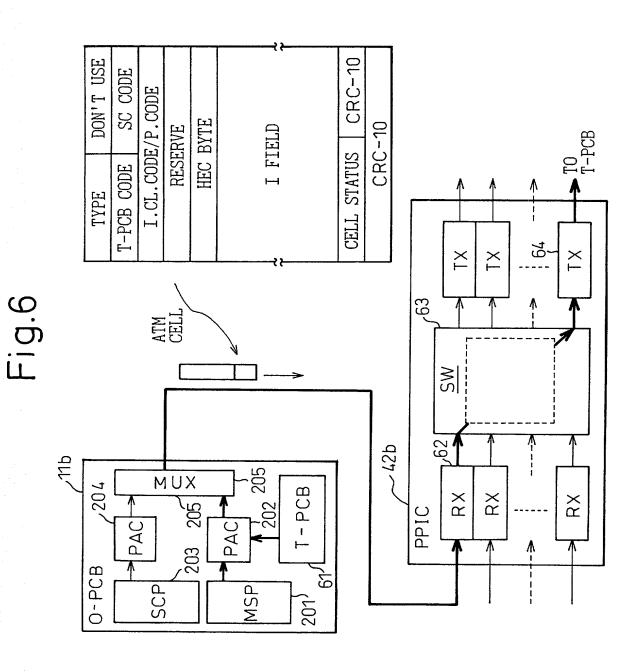


⁵/₁₇ Fig.4

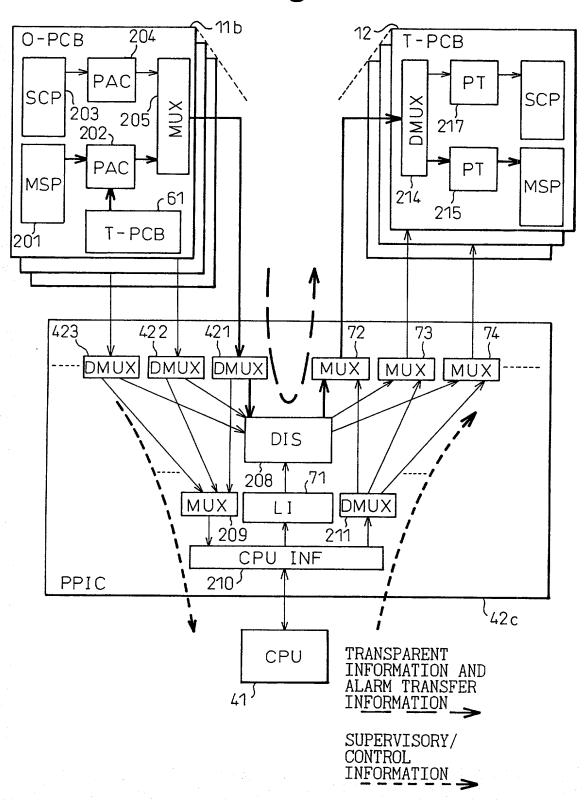




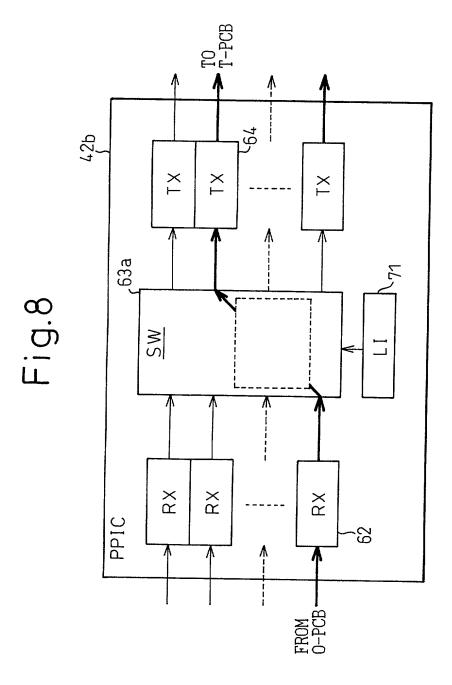




8/₁₇ Fig.7

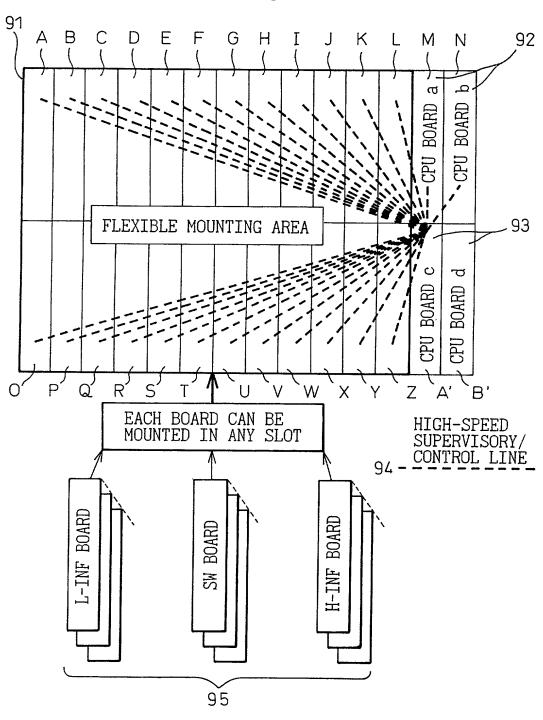


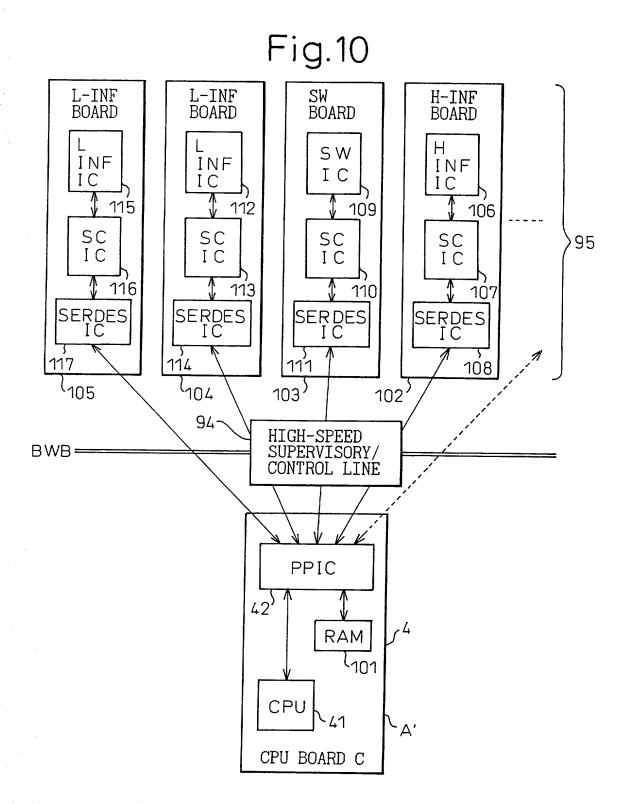




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Fig.9





12/₁₇ Fig.11

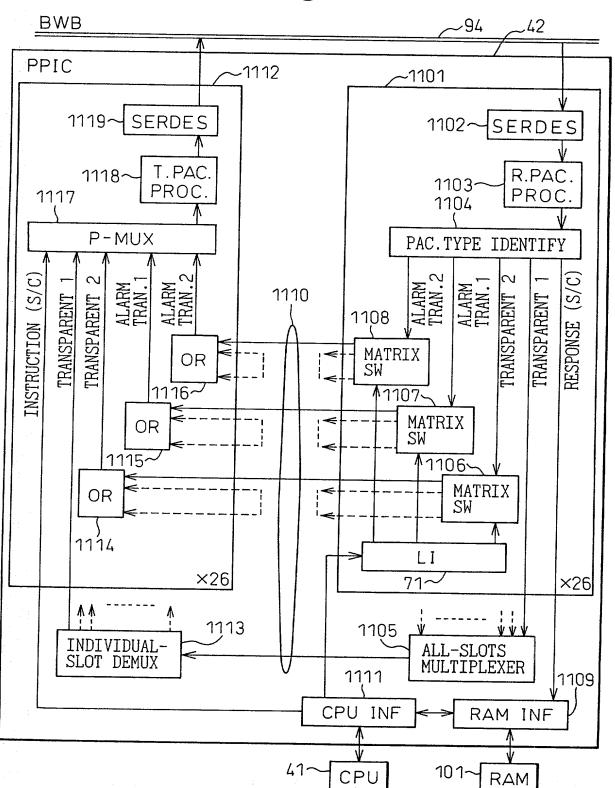
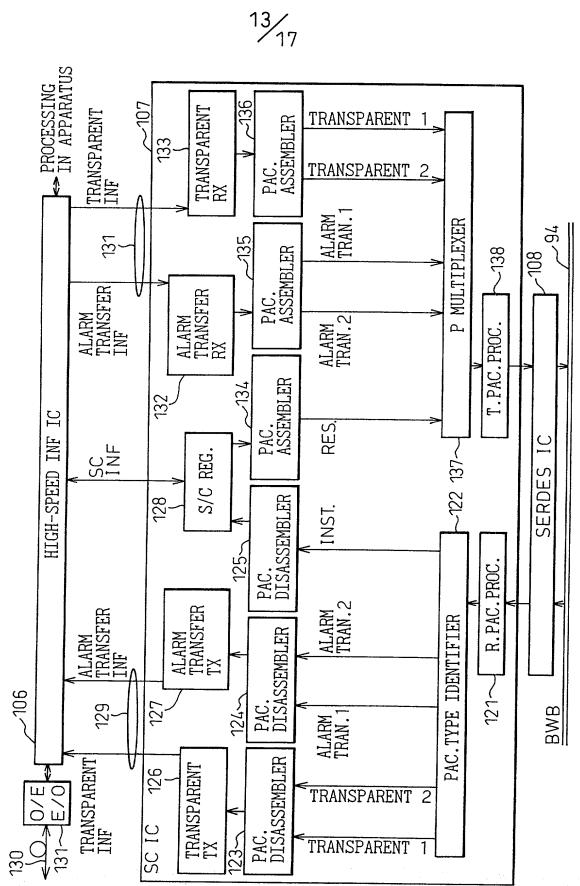


Fig.12 106



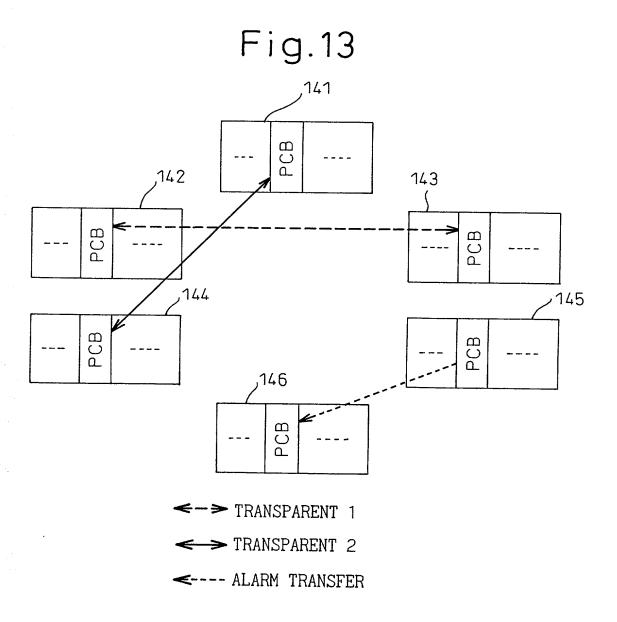
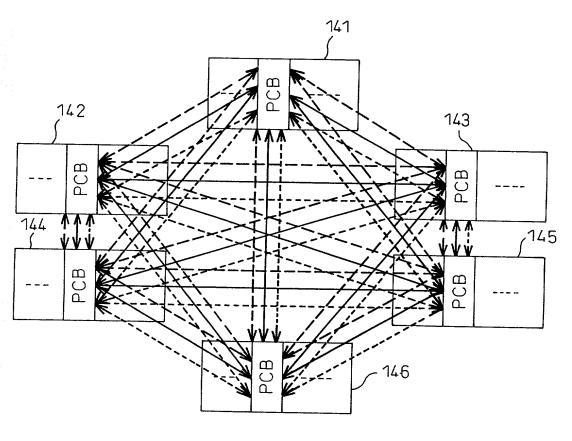
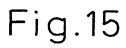
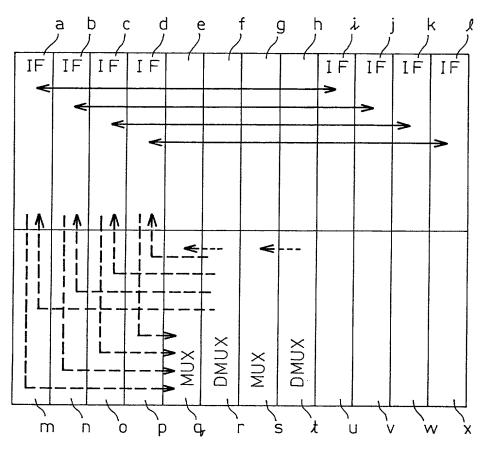


Fig.14



- ←→TRANSPARENT 1
- TRANSPARENT 2
- ←--- ALARM TRANSFER



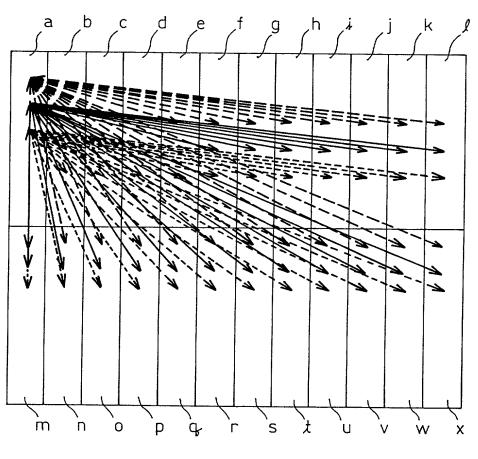


←→ TRANSPARENT 1

TRANSPARENT 2

←--- ALARM TRANSFER

Fig.16



<->TRANSPARENT 1

TRANSPARENT 2

←--- ALARM TRANSFER